

reeledmeleleearcelere elshoplelmer (account) el





Welcome **United States Patent and Trademark** Office

IEEE Xplore Milion documents Million Users

FAQ Terms IEEE Peer Help Review

Quick Links



Search

Search Results

Websters to IEEE Xp/6/78 ्रे~ संस्ताक्ष

\$22333A t C- Log-cut

()- Journals & Magazinus () Comerence

Proceedings 👉 Standarda

Search

O- 84 Vantor

Crisses (C)

()- Advanced O- Trusshel

Messher Services

Or Estatilisti IEEE

West Accessed > Access the 1EEE 8811113931 Digital Library

🕽 kasansan (in file Gibert

B Print Formul

Your search matched 10 of 1124699 documents.

A maximum of 500 results are displayed, 15 to a page, sorted by Relevance in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

systolic <and> independent <and> multiplication

... Check to search within this result set

Results Key:

JNL = Journal or Magazine CNF = Conference STD = Standard

1 A novel VLSI BiCMOS/bipolar concurrent multiplier-accumulator for DSP applications

Poornaiah, D.V.; Ananda Mohan, P.; Ahmad, M.O.; Bipolar/BiCOMS Circuits and Technology Meeting, 1993., Proceedings of the 1993, 4-5 Oct. 1993 Pages:171 - 174

[Abstract] [PDF Full-Text (480 KB)] **IEEE CNF**

2 GF(2^m) multiplication and division over the dual basis

Fenn, S.T.J.; Benaissa, M.; Taylor, D.; Computers, IEEE Transactions on , Volume: 45 , Issue: 3 , March 1996 Pages:319 - 327

[PDF Full-Text (876 KB)] [Abstract] **IEEE JNL**

3 A Class of Unidirectional Bit Serial Systolic Architectures for Multiplicative Inversion and Division over {rm GF}(2^m)

Daneshbeh, A.K.; Hasan, M.A.;

Computers, IEEE Transactions on , Volume: 54 , Issue: 3 , March 2005

Pages:370 - 380

[Abstract] [PDF Full-Text (696 KB)] IEEE JNL

4 C-Testability of Two-Dimensional Iterative Arrays

Elhuni, H.; Vergis, A.; Kinney, L.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions

on , Volume: 5 , Issue: 4 , October 1986

Pages:573 - 581

[PDF Full-Text (1184 KB)] [Abstract] IEEE JNL

5 Optimal matrix multiplication on fault-tolerant VLSI arrays

Varman, P.J.; Ramakrishnan, I.V.;

Computers, IEEE Transactions on , Volume: 38 , Issue: 2 , Feb. 1989

Pages: 278 - 283

[PDF Full-Text (596 KB)] IEEE JNL [Abstract]

6 Systolic digit-serial multiplier

Ashur, A.S.; Ibrahim, M.K.; Aggoun, A.;

Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-

Circuits, Devices and Systems], Volume: 143, Issue: 1, Feb. 1996 Pages:14 - 20

[Abstract] [PDF Full-Text (584 KB)] IEE JNL

7 VLSI implementation of modulo multiplication using carry free addition

Sarkar, P.; Roy, B.K.; Choudhury, P.P.; VLSI Design, 1997. Proceedings., Tenth International Conference on , 4-7 Jan. 1997 Pages:457 - 460

[Abstract] [PDF Full-Text (400 KB)] IEEE CNF

8 A three dimensional systolic array architecture for fast matrix multiplication

Linderman, R.; Ku, W.; Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '84., Volume: 9, Mar 1984 Pages: 782 - 785

[Abstract] [PDF Full-Text (81 KB)] IEEE CNF

9 An efficient and flexible bit-level systolic array for inner product computation

Chin-Liang Wang; Chi-Mo Hwang; TENCON 90. 1990 IEEE Region 10 Conference on Computer and Communication Systems, 24-27 Sept. 1990 Pages: 284 - 288 vol.1

[Abstract] [PDF Full-Text (384 KB)] IEEE CNF

10 Constant testability for single fault detection in two-dimensional systolic array structures for matrix multiplication

Lombardi, F.; Sciuto, D.; Huang, W.; Circuits and Systems, 1990., IEEE International Symposium on , 1-3 May 1990

Pages:2728 - 2731 vol.4

[Abstract] [PDF Full-Text (328 KB)] IEEE CNF

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help |

FAQ | Terms | Back to Top

Copyright © 2004 IEEE — All rights reserved